

Refine Search

Search Results -

Terms	Documents
L2 and L3 and L4 and L6	41

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L7



Refine Search

Recall Text

Clear

Interrupt

Search History

 DATE: Friday, January 27, 2006 [Printable Copy](#) [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>		
<u>L7</u>	l2 and l3 and l4 and L6	41	<u>L7</u>
<u>L6</u>	time with (disk or disc or memory or storage)	667206	<u>L6</u>
<u>L5</u>	time with disk	86053	<u>L5</u>
<u>L4</u>	time near7 (replicat\$4 or duplicat\$4 or backup or (back\$4 adj up))	30249	<u>L4</u>
<u>L3</u>	time near7 retriev\$4	43855	<u>L3</u>
<u>L2</u>	(dynami\$6 or automatic\$5) with cach\$4 with (characteristic or factor or weight or cost or parameter or frequenc\$4 or recenc\$4 or time or indicat\$4)	1696	<u>L2</u>
<u>L1</u>	(dynami\$6 or automatic\$5) with cach\$4 with (characteristic or factor or weight or cost or parameter or frequenc\$4 or recenc\$4 or time)	1444	<u>L1</u>

END OF SEARCH HISTORY



Welcome United States Patent and Trademark Office

Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)[SUP](#)

Results for "((((dynamically<near/5>caching)<paragraph>(characteristics<or>factors<or>wel..."

Your search matched 7 of 1306777 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

e-mail

» Search Options

[View Session History](#)[New Search](#)

Modify Search

☐ Check to search only within this results set

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

 Display Format: ☒ Citation ☐ Citation & Abstract

Select Article Information

- | | |
|--------------------------|---|
| <input type="checkbox"/> | <p>1. Design of an adaptive cache coherence protocol for large scale multiprocessors
 Yang, Q.; Thangadurai, G.; Bhuyan, L.N.;
 Parallel and Distributed Systems, IEEE Transactions on
 Volume 3, Issue 3, May 1992 Page(s):281 - 293
 Digital Object Identifier 10.1109/71.139202
 AbstractPlus Full Text: PDF(1132 KB) IEEE JNL</p> |
| <input type="checkbox"/> | <p>2. QoS-adaptive proxy caching for multimedia streaming over the Internet
 Fang Yu; Qian Zhang; Wenwu Zhu; Ya-Qin Zhang;
 Circuits and Systems for Video Technology, IEEE Transactions on
 Volume 13, Issue 3, March 2003 Page(s):257 - 269
 Digital Object Identifier 10.1109/TCSVT.2003.809829
 AbstractPlus References Full Text: PDF(967 KB) IEEE JNL</p> |
| <input type="checkbox"/> | <p>3. Fuzzy logic routing with load-balancing using a realistic mobility model
 Rea, S.; Pesch, D.;
 Vehicular Technology Conference, 2005. VTC 2005-Spring. 2005 IEEE 61st
 Volume 4, 30 May-1 June 2005 Page(s):2611 - 2615 Vol. 4
 Digital Object Identifier 10.1109/VETECS.2005.1543807
 AbstractPlus Full Text: PDF(4432 KB) IEEE CNF</p> |
| <input type="checkbox"/> | <p>4. A novel fault tolerant cache to improve yield in nanometer technologies
 Agarwal, A.; Paul, B.C.; Roy, K.;
 On-Line Testing Symposium, 2004. IOLTS 2004. Proceedings. 10th IEEE International
 12-14 July 2004 Page(s):149 - 154
 AbstractPlus Full Text: PDF(366 KB) IEEE CNF</p> |
| <input type="checkbox"/> | <p>5. Exploiting program hotspots and code sequentiality for instruction cache leakage management
 Hu, J.S.; Nadgir, A.; Vijaykrishnan, N.; Irwin, M.J.; Kandemir, M.;
 Low Power Electronics and Design, 2003. ISLPED '03. Proceedings of the 2003 International Symposium
 25-27 Aug. 2003 Page(s):402 - 407
 Digital Object Identifier 10.1109/LPE.2003.1231936
 AbstractPlus Full Text: PDF(762 KB) IEEE CNF</p> |
| <input type="checkbox"/> | <p>6. Query scheduling in multi query optimization
 Gupta, A.; Sudarshan, S.; Vishwanathan, S.;
 Database Engineering & Applications, 2001 International Symposium on.</p> |

16-18 July 2001 Page(s):11 - 19

Digital Object Identifier 10.1109/IDEAS.2001.938067

[AbstractPlus](#) | Full Text: [PDF](#)(672 KB) IEEE CNF



7. An adaptive cache coherence scheme for hierarchical shared-memory multiprocessors

Qing Yang; Thangadurai, G.; Bhuyan, L.N.;

Parallel and Distributed Processing, 1990. Proceedings of the Second IEEE Symposium on
9-13 Dec. 1990 Page(s):318 - 325

Digital Object Identifier 10.1109/SPDP.1990.143556

[AbstractPlus](#) | Full Text: [PDF](#)(824 KB) IEEE CNF



Indexed by
 Inspec

[Help](#) [Contact Us](#) [Privacy & Se](#)

© Copyright 2005 IEEE – A